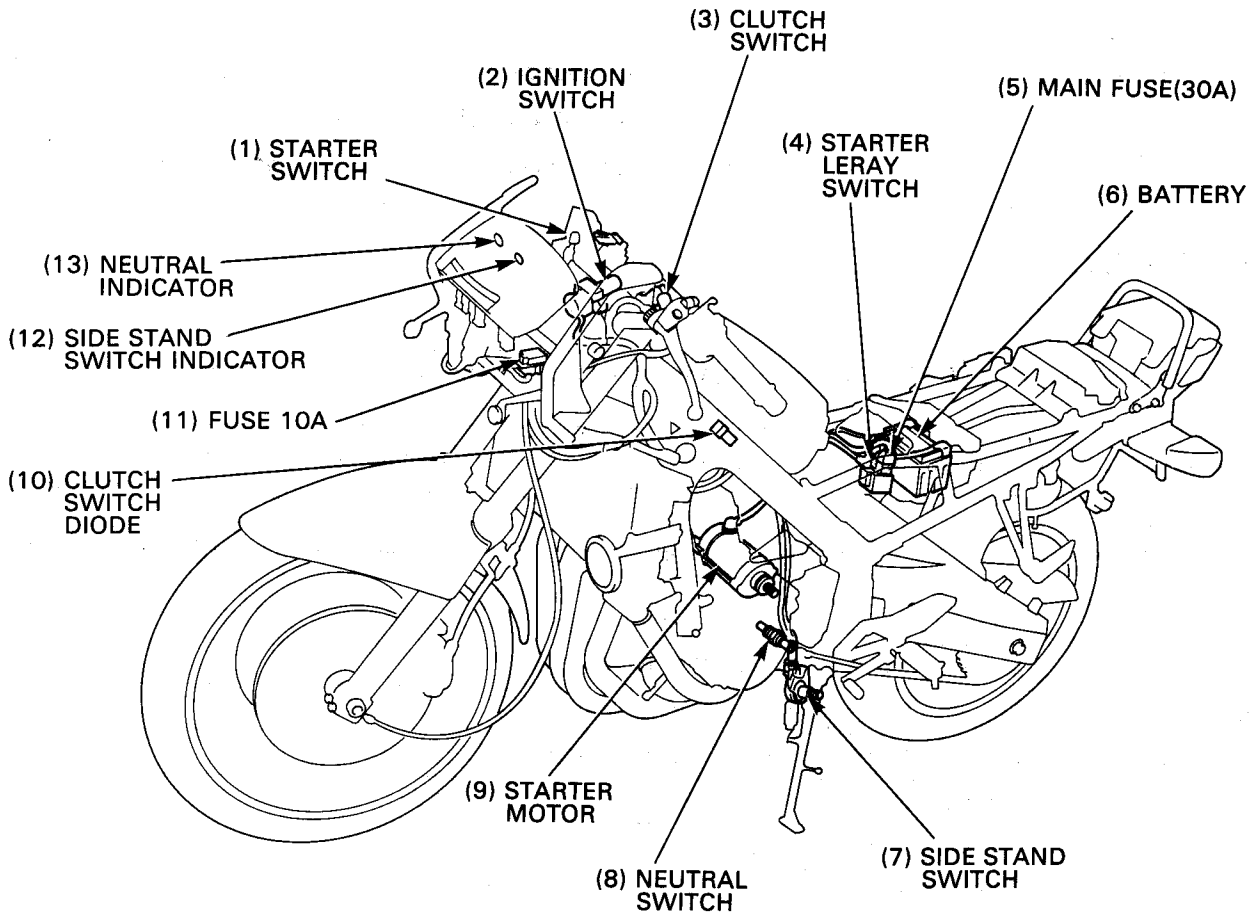


System Location



Circuit Diagram

